

107



FIG. 1

32

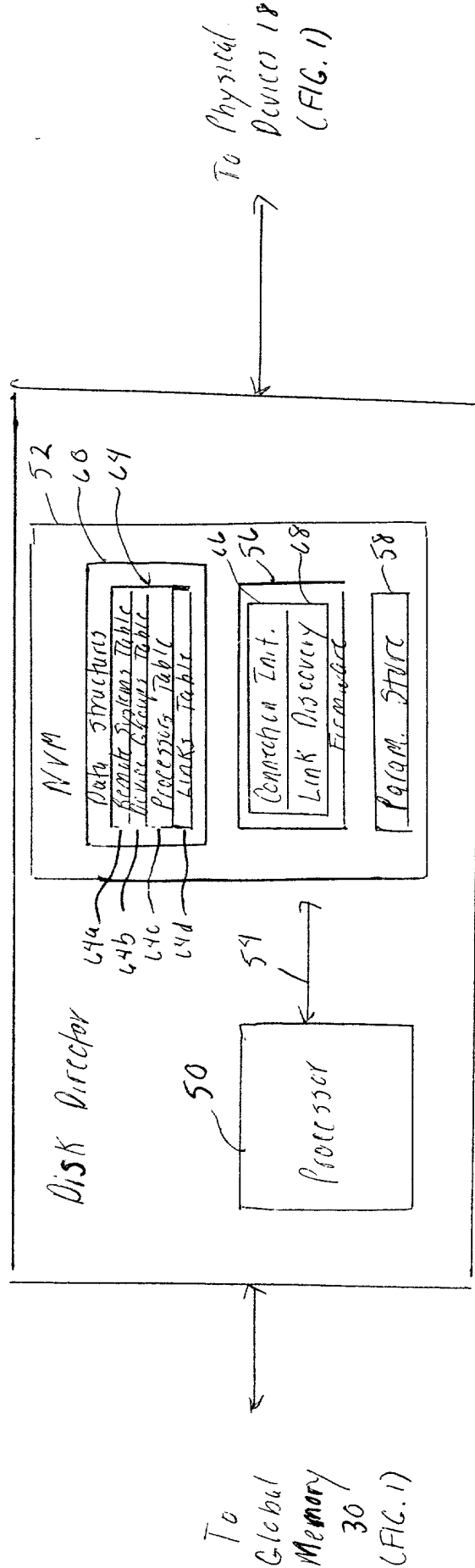


FIG. 2

64b

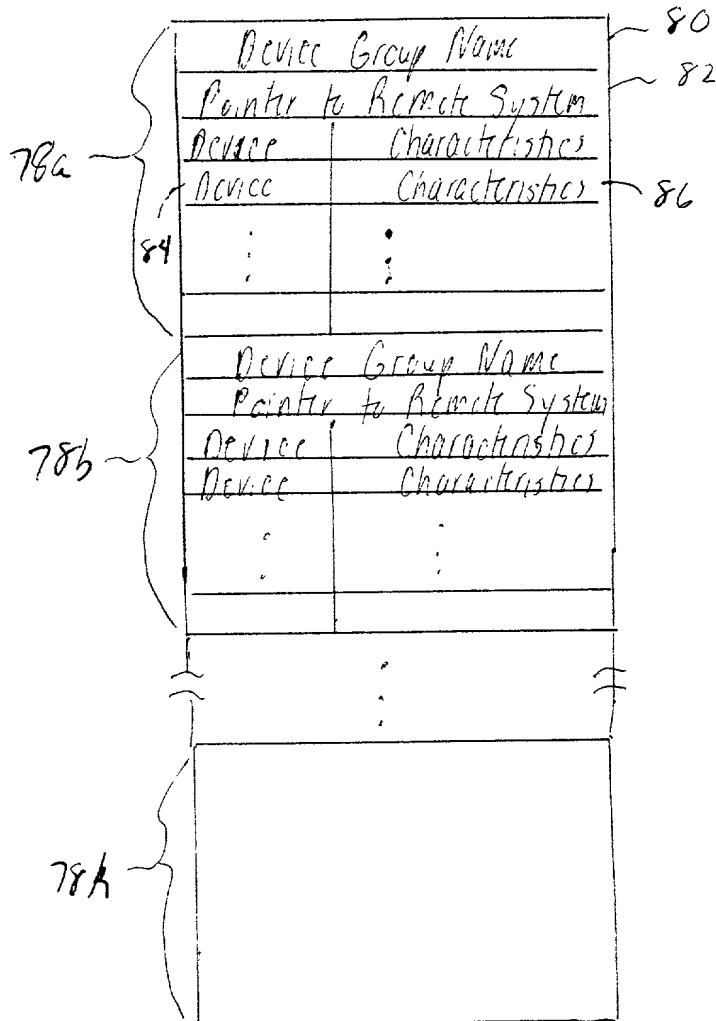


FIG. 3B

64a

70	72 Serial No	74 Model	76 Code Level

FIG. 3A

64c

90	92 Name (IP or WWN)	94 Pointer(s) to Device Group(s)

FIG. 3C

64d

98	100 Link State	102 Pointer to Director/Proc.	104 Pointer to Device Group

FIG. 3D

106

106

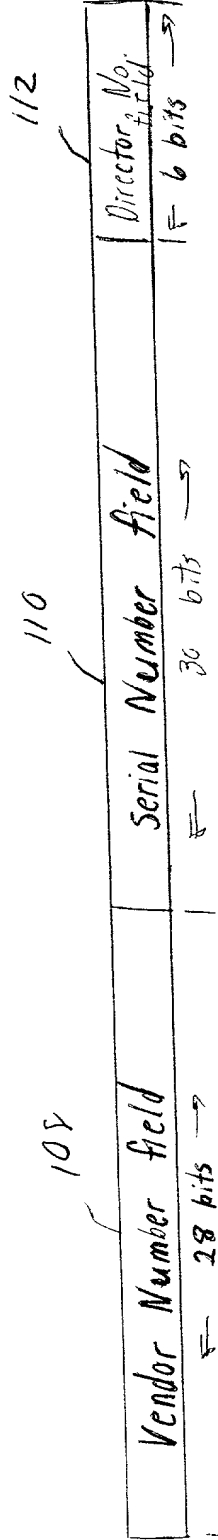


FIG. 4

10

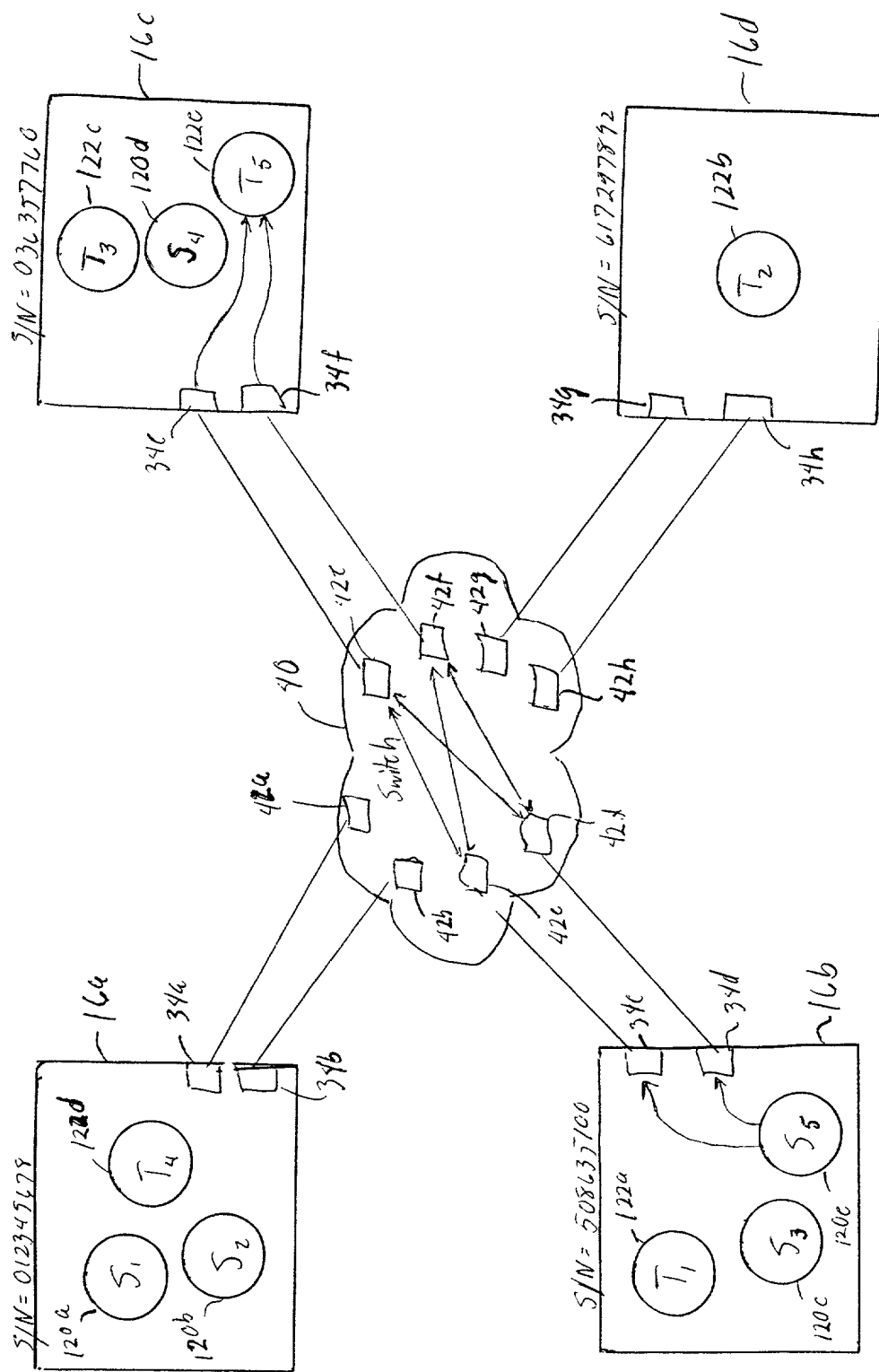


FIG. 5

FIG. 6 is a block diagram of a system architecture showing the flow of data from Logical Links to Remote Box Information.

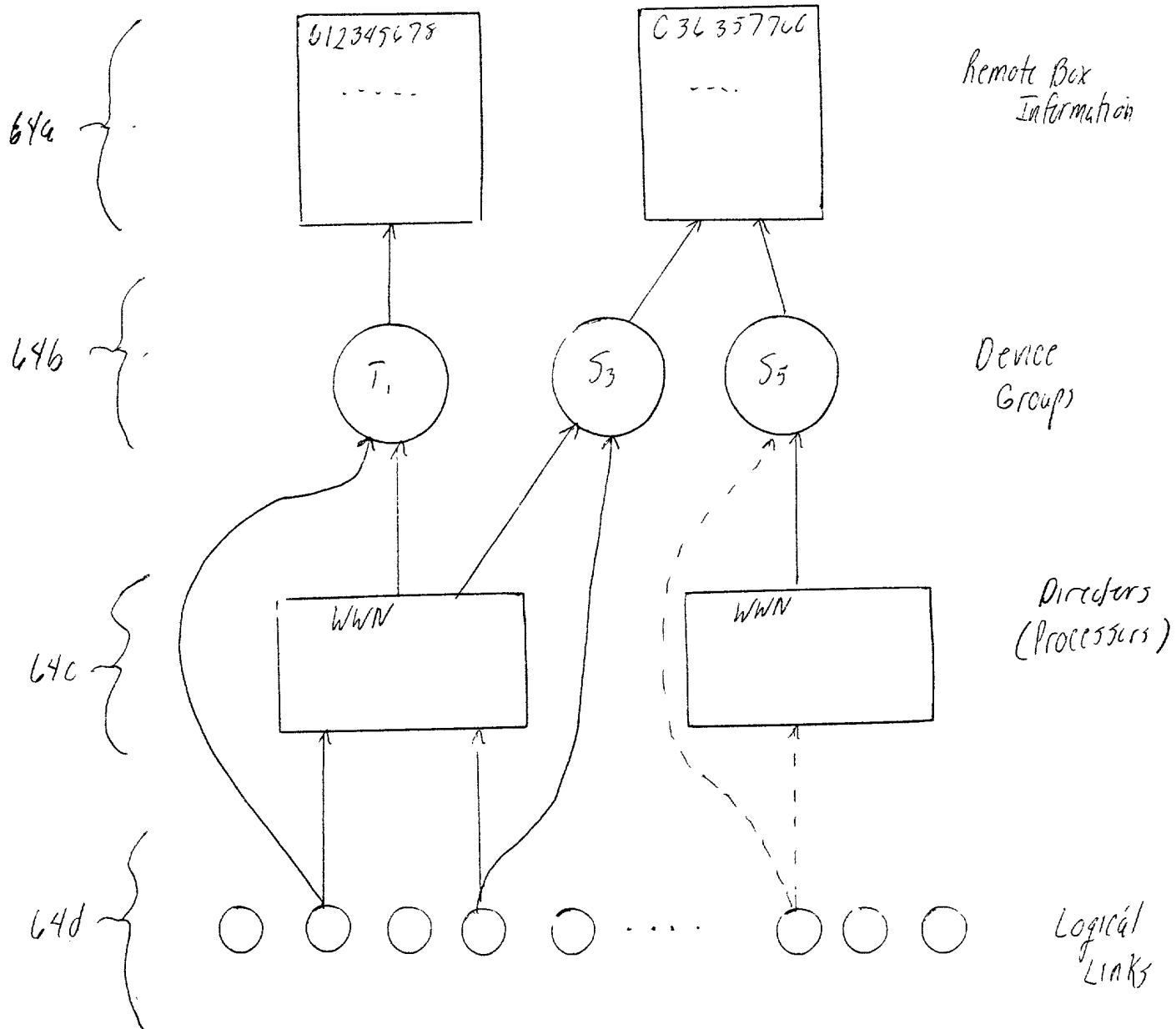


FIG. 6

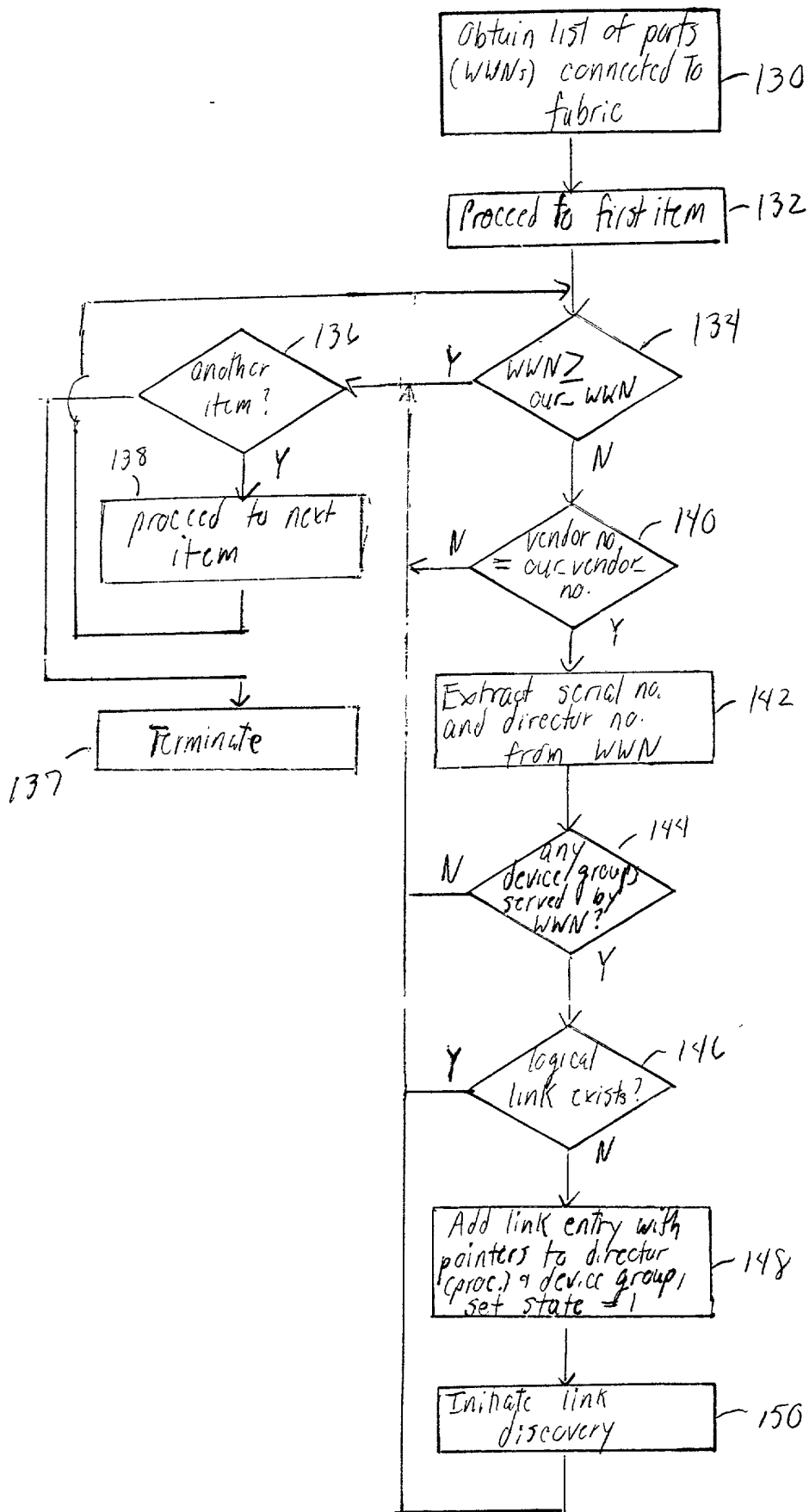


FIG. 7

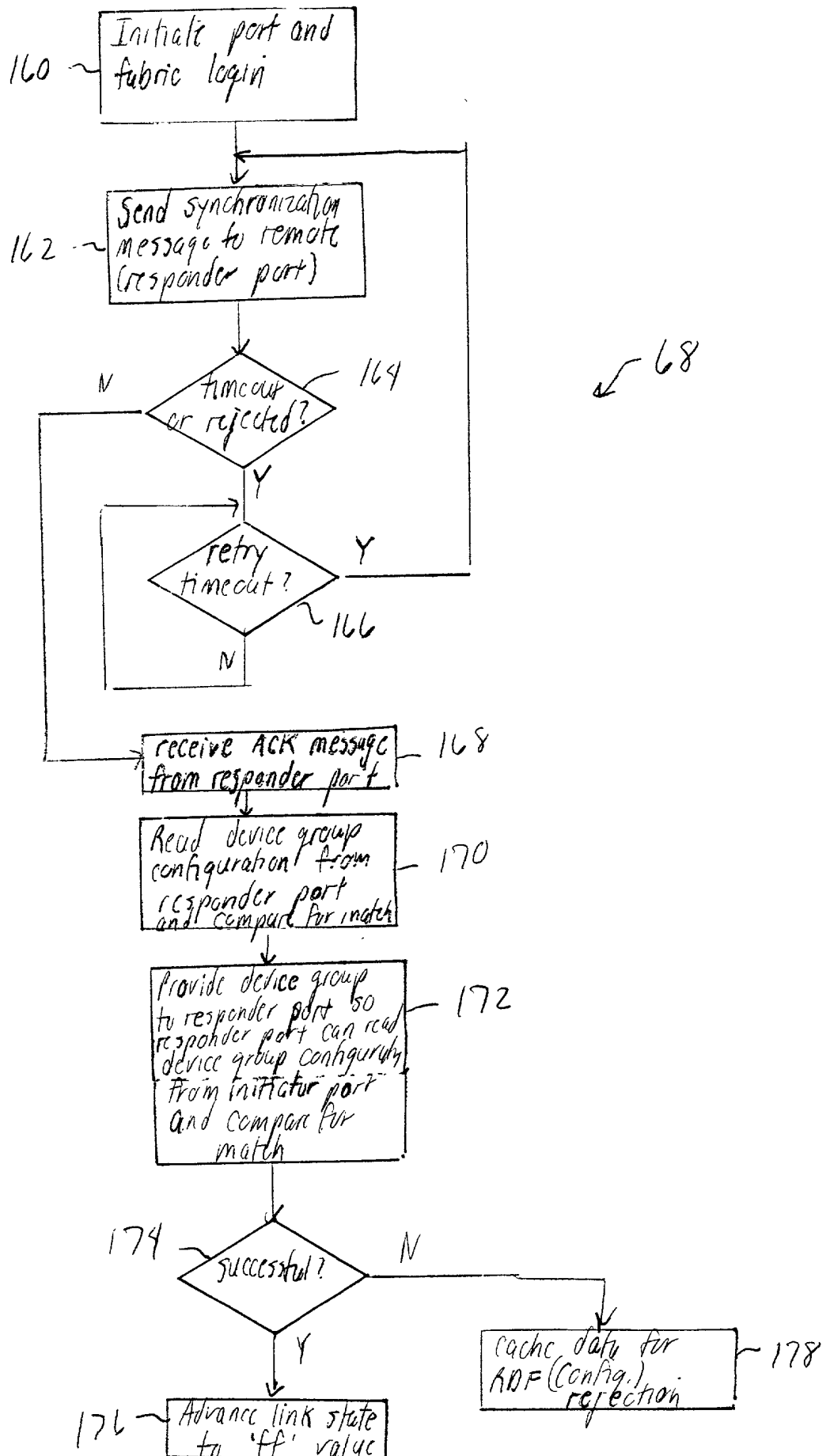


FIG. 8